SECURITIES

SECURITI	ES AND EXCHANGE COMMISSION Washington, DC 20549
	FORM 6-K
PU	EPORT OF FOREIGN PRIVATE ISSUER URSUANT TO RULE 13a-16 OR 15d-16 OF E SECURITIES EXCHANGE ACT OF 1934
	For the month of January, 2006
ChipMOS T	ECHNOLOGIES (Bermuda) LTD. (Translation of Registrant s Name Into English)
	11F, No. 3, Lane 91, Dongmei Road Hsinchu, Taiwan

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Form 20-F <u>ü</u> Form 40-F
(Indicate by check mark whether the registrant by furnishing the information contained in this form is also thereby furnishing the information to the Commission pursuant to Rule 12g3-2(b) under the Securities Exchange Act of 1934.)
Yes No <u>ü</u>
(If Yes is marked, indicate below the file number assigned to the registrant in connection with Rule 12g3-2(b): 82)
THIS REPORT ON FORM 6-K SHALL BE DEEMED TO BE INCORPORATED BY REFERENCE IN THE PROSPECTUS INCLUDED IN THE REGISTRATION STATEMENT ON FORM F-3 (FILE NO.333-130230) OF ChipMOS TECHNOLOGIES (BERMUDA) LTD. AND TO BE A PART THEREOF FROM THE DATE ON WHICH THIS REPORT IS FURNISHED, TO THE EXTENT NOT SUPERSEDED BY DOCUMENTS OR REPORTS SUBSEQUENTLY FILED OR FURNISHED.

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SIGNATURES

Pursuant to the requirements of the Securities Exchange Act of 1934, the registrant has duly caused this report to be signed on its behalf by the undersigned, thereunto duly authorized.

ChipMOS TECHNOLOGIES (Bermuda) LTD.

(Registrant)

Date: January 26, 2006 By /S/ S.J. Cheng

Name: S. J. Cheng

Title: Chairman & Chief Executive Officer

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EXHIBITS

Exhibit

Number

1.1 Press Release

Exhibit 1.1

Contacts:

In Taiwan R.O.C. In the U.S.

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ChipMOS AND RYOWA ENTER INTO BGA SUBSTRATE SUPPLY AGREEMENT

Hsinchu, Taiwan, January 26, 2006 ChipMOS TECHNOLOGIES (Bermuda) LTD. (ChipMOS) (Nasdaq: IMOS) announced today that its 69.8% owned subsidiary, ChipMOS TECHNOLOGIES INC. (ChipMOS Taiwan), has signed a BGA (Ball Grid Array) substrate supply agreement with RYOWA CO., LTD. (RYOWA), a major supplier. Under this one-year supply agreement, RYOWA will provide a minimum of 30 million pieces of BGA substrates per month. This will be used mainly for manufacturing DDR II memory packages.

S.J. Cheng, Chairman and Chief Executive Officer of ChipMOS, said, We are encouraged to see the recent pick-up in DDR II memory finally happening after several years of delays. This will likely become mainstream around the end of first half of this year. Based on our customers current forecasts, it was necessary to secure a high quality, high volume supply of BGA substrate to meet the anticipated increasing demand for DDR II packaging.

About ChipMOS TECHNOLOGIES (Bermuda) LTD.:

ChipMOS (http://www.chipmos.com.tw/) is a leading independent provider of semiconductor testing and assembly services to customers in Taiwan, Japan, and the U.S. With advanced facilities in Hsinchu and Southern Taiwan Science Parks in Taiwan and Shanghai, ChipMOS and its subsidiaries provide testing and assembly services to a broad range of customers, including leading fabless semiconductor companies, integrated device manufacturers and independent semiconductor foundries.

Forward-Looking Statements

Certain statements contained in this announcement may be viewed as forward-looking statements within the meaning of Section 27A of the U.S. Securities Act of 1933, as amended, and Section 21E of the U.S. Securities Exchange Act of 1934, as amended. Such forward-looking statements involve known and unknown risks, uncertainties and other factors, which may cause the actual performance, financial condition or results of operations of the Company to be materially different from any future performance, financial condition or results of operations implied by such forward-looking statements. Further information regarding these risks, uncertainties and other factors is included in the Company s most recent Annual Report on Form 20-F filed with the U.S. Securities and Exchange Commission (the SEC) and in the Company s other filings with the SEC.